FIG. 3B, in conjunction with FIG. 3A, illustrates a recharge network;

FIG. 3C illustrates a constant current sink control network; and

FIG. 4 illustrates a precision voltage reference generation diagram. – –

At page 5, line 18, delete "hold capacitors 58" and insert – – hold capacitor 58, as shown in FIG. 3A. – –.

At page 6, line 10, after "circuitry 10 " insert – – of FIG. 3. – –.

At page 8, line 3, after delete "capacitors" and insert – – capacitor – –.

At page 8, line 22, delete "12, 14, 16, 18" and insert – – 12 and 14 (FIG 3A) and 14 and 16 (FIG. 3B) – –.

At page 10, line 12, after "70 – 96" insert – – as shown in FIG. 3C – –.

At page 11, line 11, after "112, 114" insert – – as shown in FIG. 3C – –.

IN THE CLAIMS:

Please cancel claim 17 without prejudice.

Please add new claims 22 as set forth below.

Please amend claims 1, 9, 12, 15, 16, and 18 as follows:

1. (Amended) A linear ramp generation circuit operating in a recovery mode, a ramp mode, or a hold mode, said circuit comprising:

an output node;

a first input node coupled to an externally provided first signal;

a second input node coupled to an externally provided second input signal;

a constant current source network;